

**ABSTRACT**

The invention relates to Method to detect a sluggishness or the blocking of an electric drive (3), which is triggered via a power semiconductor component (7). The electric drive (3) can be  
5 operated in both a partial load range (PWM timing signal < 100%) as well as in a full load range (PWM timing signal = 100%), but always as a function of the timing of the PWM signal (29, 30). An evaluation circuit (37) is connected with a micro-controller (25). Pulses generated within a time interval are detected in the evaluation circuit (37) from the current I flowing via the first power semiconductor component (7) and the number of detected pulses A1\*, A2\* are compared with a to-  
10 be-expected number of pulses A1, A2.